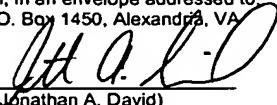




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Dated: April 18, 2006

Signature: 

(Jonathan A. David)

Docket No.: SCEI 3.0-187
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Inoue et al.

Application No.: 10/783,238

Group Art Unit: 2127

Filed: February 20, 2004

Examiner: MENG AN

For: METHODS AND APPARATUS FOR
PROCESSOR TASK MIGRATION IN A
MULTI-PROCESSOR SYSTEM

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

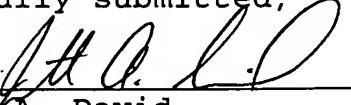
Dear Sir:

It is respectfully requested that the references listed on the enclosed form be made of record and considered with respect to the above-referenced U.S. patent application. A copy of each non-US reference is enclosed. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

In the event that any fee is due in connection with the present Information Disclosure Statement, the Commissioner is hereby authorized to charge the same to our Deposit Account No. 12-1095.

Dated: April 18, 2006

Respectfully submitted,

By 
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Substitute for form 1449A/B/PTO				Complete if Known	
				Application Number	10/783,238-Conf. #1362
				Filing Date	February 20, 2004
				First Named Inventor	Keisuke Inoue
				Art Unit	2127
				Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	SCEI 3.0-187

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	BA	EP-0 750 255-A	12-27-1996	DATA GENERAL CORPORATION	<input checked="" type="checkbox"/> T ⁶

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	CA	PCT/ISA/210 International Search Report dated November 14, 2005			<input checked="" type="checkbox"/> **
	CB	BLACK D L, "Scheduling Support for Concurrency and Parallelism in the Mach Operating System", Computer IEEE Service Center, Los Alamitos, Ca., Vol. 23, No. 5, May 1, 1990, pages 35-43.			

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**Issued in foreign application corresponding to U.S. Application No. 10/783,246.

Examiner Signature		Date Considered	
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